

FORM PTO-1449 (REV. 7-80)	MAR 2 3 1998 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 660082.419A	APPLICATION NO. 08/735,729
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)		APPLICANT Dean A. Klein	
		FILING DATE October 23, 1996	GROUP ART UNIT 2316 2787

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						

FOREIGN PATENT DOCUMENTS

	AG	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AH						RO	NO
	AI						IP	NO
	AJ						2600	NO
	AK						DE	NO

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

KK	AL	Compaq Computer Corporation; Phoenix Technologies Ltd.; and Intel Corporation "Plug and Play BIOS Specification, Version 1.0A" 56pp, May 5, 1994
KK	AM	Intel Corporation; Microsoft Corporation "Plug and Play ISA Specification, Version 1.0a" 66pp, May 5, 1994
KK	AN	Shanley, Tom/Anderson, Don, Chap. VII, "The Power-Up Sequence," pp. 113-117; Chap. XV, "ROM Memory," pp. 315-332; Chap. XXI, "RTC and Configuration RAM," pp. 441-447, <i>ISA SYSTEM ARCHITECTURE</i> , 3 rd Edition, Addison-Wesley Publishing Co., U.S., 1995.

EXAMINER

DATE CONSIDERED

9-22-98

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).